ELEC677
Advanced Electronics Engineering
S1 Day 2017
Dept of Engineering

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General Information

Unit convenor and teaching staff
Sourabh Khandelwal
sourabh.khandelwal@mq.edu.au

Oya Sevimli
oya.sevimli@mq.edu.au

Credit points
4

Prerequisites
Admission to MEng

Corequisites

Co-badged status
ELEC476

Unit description
This unit integrates prior learning in a specialist area of engineering with problem solving, emerging technology and aspects of engineering application, technical reporting and self-management to prepare students to work at a professional capacity. The unit aims to address the application of fundamental principles and methods at an advanced level in the context of standards and practices, modelling, analysis, design and practical implementation. The unit also develops skills in the critical evaluation of information, software and sources of error and experimental methods. Learning will be achieved using case studies, laboratories, presentations, group work and traditional lecture format. The specific topics will focus on current advances in the area including advanced electronics systems such as PLLs, oscillators, analogue-to-digital conversion, power conversion and control, IC design, radio circuits and systems, RF measurements, and CAD.

Important Academic Dates
Information about important academic dates including deadlines for withdrawing from units are available at http://students.mq.edu.au/student_admin/enrolmentguide/academicdates/

Learning Outcomes

1. Understand operation of MOSFETs and their mathematical models.
2. Apply understanding of MOSFET operation for design of CMOS logic circuits
3. Ability to simulate and design digital CMOS circuits using EDA tools
4. Apply MOSFET understanding to analog circuits
5. Understand the concepts of feedback and noise in analog circuits
6. Ability to simulate and design analog CMOS circuits using EDA tools

Assessment Tasks

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<thead>
<tr>
<th>Name</th>
<th>Weighting</th>
<th>Due</th>
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<tr>
<td>Home Assignments</td>
<td>15%</td>
<td>TBD</td>
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<tr>
<td>In-Class Mid Term Test</td>
<td>25%</td>
<td>TBD</td>
</tr>
<tr>
<td>Final Exam</td>
<td>40%</td>
<td>TBD</td>
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<tr>
<td>Practicals</td>
<td>20%</td>
<td>TBD</td>
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Home Assignments
Due: TBD
Weighting: 15%

Assignments will be given for each module of this course. Students will submit answers to the questions asked in assignments.

This Assessment Task relates to the following Learning Outcomes:
• Understand operation of MOSFETs and their mathematical models.
• Apply understanding of MOSFET operation for design of CMOS logic circuits
• Apply MOSFET understanding to analog circuits
• Understand the concepts of feedback and noise in analog circuits

In-Class Mid Term Test
Due: TBD
Weighting: 25%

In-class test at the mid term.

This Assessment Task relates to the following Learning Outcomes:
• Understand operation of MOSFETs and their mathematical models.
• Apply understanding of MOSFET operation for design of CMOS logic circuits
• Apply MOSFET understanding to analog circuits
• Understand the concepts of feedback and noise in analog circuits
Final Exam
Due: TBD
Weighting: 40%
3 hours final exam.

This Assessment Task relates to the following Learning Outcomes:
- Understand operation of MOSFETs and their mathematical models.
- Apply understanding of MOSFET operation for design of CMOS logic circuits
- Apply MOSFET understanding to analog circuits
- Understand the concepts of feedback and noise in analog circuits

Practicals
Due: TBD
Weighting: 20%

Practicals will be on simulations using EDA tools on different modules. Students should submit reports on the laboratory/simulation work.
Format for reports will be given in iLearn.

This Assessment Task relates to the following Learning Outcomes:
- Ability to simulate and design digital CMOS circuits using EDA tools
- Ability to simulate and design analog CMOS circuits using EDA tools

Delivery and Resources
Text Books:
Sedra and Smith "Microelectronic Circuits", Cambridge University Press.
Ben G. Streetman and S. Banerjee "Solid State Electronics Devices", Pearson

Reference Books:
Series of engineering journal references

Notes:
Lecture notes will be provided

EDA tools:
AWR will be provided for simulations
Unit Schedule
Check in iLearn

Policies and Procedures
Macquarie University policies and procedures are accessible from Policy Central. Students should be aware of the following policies in particular with regard to Learning and Teaching:

Academic Honesty Policy http://mq.edu.au/policy/docs/academic_honesty/policy.html
Disruption to Studies Policy http://www.mq.edu.au/policy/docs/disruption_studies/policy.html The Disruption to Studies Policy is effective from March 3 2014 and replaces the Special Consideration Policy.

In addition, a number of other policies can be found in the Learning and Teaching Category of Policy Central.

Student Code of Conduct
Macquarie University students have a responsibility to be familiar with the Student Code of Conduct: https://students.mq.edu.au/support/student_conduct/

Results
Results shown in iLearn, or released directly by your Unit Convenor, are not confirmed as they are subject to final approval by the University. Once approved, final results will be sent to your student email address and will be made available in eStudent. For more information visit ask.mq.edu.au.

Student Support
Macquarie University provides a range of support services for students. For details, visit http://students.mq.edu.au/support/

Learning Skills
Learning Skills (mq.edu.au/learningskills) provides academic writing resources and study strategies to improve your marks and take control of your study.

• Workshops
• StudyWise
• Academic Integrity Module for Students
• Ask a Learning Adviser
Graduate Capabilities

Discipline Specific Knowledge and Skills

Our graduates will take with them the intellectual development, depth and breadth of knowledge, scholarly understanding, and specific subject content in their chosen fields to make them competent and confident in their subject or profession. They will be able to demonstrate, where relevant, professional technical competence and meet professional standards. They will be able to articulate the structure of knowledge of their discipline, be able to adapt discipline-specific knowledge to novel situations, and be able to contribute from their discipline to inter-disciplinary solutions to problems.

This graduate capability is supported by:

Learning outcomes

- Understand operation of MOSFETs and their mathematical models.
- Apply understanding of MOSFET operation for design of CMOS logic circuits
- Ability to simulate and design digital CMOS circuits using EDA tools
- Apply MOSFET understanding to analog circuits
- Understand the concepts of feedback and noise in analog circuits
- Ability to simulate and design analog CMOS circuits using EDA tools

Assessment tasks

- Home Assignments
- In-Class Mid Term Test
- Final Exam
- Practicals
Problem Solving and Research Capability

Our graduates should be capable of researching; of analysing, and interpreting and assessing data and information in various forms; of drawing connections across fields of knowledge; and they should be able to relate their knowledge to complex situations at work or in the world, in order to diagnose and solve problems. We want them to have the confidence to take the initiative in doing so, within an awareness of their own limitations.

This graduate capability is supported by:

**Learning outcomes**

- Understand operation of MOSFETs and their mathematical models.
- Apply understanding of MOSFET operation for design of CMOS logic circuits
- Ability to simulate and design digital CMOS circuits using EDA tools
- Apply MOSFET understanding to analog circuits
- Understand the concepts of feedback and noise in analog circuits
- Ability to simulate and design analog CMOS circuits using EDA tools

**Assessment tasks**

- Home Assignments
- In-Class Mid Term Test
- Final Exam
- Practicals

Effective Communication

We want to develop in our students the ability to communicate and convey their views in forms effective with different audiences. We want our graduates to take with them the capability to read, listen, question, gather and evaluate information resources in a variety of formats, assess, write clearly, speak effectively, and to use visual communication and communication technologies as appropriate.

This graduate capability is supported by:

**Learning outcomes**

- Ability to simulate and design digital CMOS circuits using EDA tools
- Ability to simulate and design analog CMOS circuits using EDA tools

**Assessment task**

- Practicals
Critical, Analytical and Integrative Thinking

We want our graduates to be capable of reasoning, questioning and analysing, and to integrate and synthesise learning and knowledge from a range of sources and environments; to be able to critique constraints, assumptions and limitations; to be able to think independently and systemically in relation to scholarly activity, in the workplace, and in the world. We want them to have a level of scientific and information technology literacy.

This graduate capability is supported by:

**Learning outcomes**

* Understand the concepts of feedback and noise in analog circuits
* Ability to simulate and design analog CMOS circuits using EDA tools

**Assessment tasks**

* Home Assignments
* In-Class Mid Term Test
* Final Exam
* Practicals

Creative and Innovative

Our graduates will also be capable of creative thinking and of creating knowledge. They will be imaginative and open to experience and capable of innovation at work and in the community. We want them to be engaged in applying their critical, creative thinking.

This graduate capability is supported by:

**Learning outcomes**

* Apply understanding of MOSFET operation for design of CMOS logic circuits
* Ability to simulate and design digital CMOS circuits using EDA tools
* Apply MOSFET understanding to analog circuits
* Understand the concepts of feedback and noise in analog circuits

**Assessment tasks**

* Home Assignments
* In-Class Mid Term Test
* Final Exam
* Practicals

Changes from Previous Offering

Learning outcomes have been revised.
Assessments have been revised.