

# **ELEC8040**

# VLSI, Algorithms, and Systems

Session 2, Special circumstances 2021

School of Engineering

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#### Disclaimer

Macquarie University has taken all reasonable measures to ensure the information in this publication is accurate and up-to-date. However, the information may change or become out-dated as a result of change in University policies, procedures or rules. The University reserves the right to make changes to any information in this publication without notice. Users of this publication are advised to check the website version of this publication [or the relevant faculty or department] before acting on any information in this publication.

#### Session 2 Learning and Teaching Update

The decision has been made to conduct study online for the remainder of Session 2 for all units WITHOUT mandatory on-campus learning activities. Exams for Session 2 will also be online where possible to do so.

This is due to the extension of the lockdown orders and to provide certainty around arrangements for the remainder of Session 2. We hope to return to campus beyond Session 2 as soon as it is safe and appropriate to do so.

Some classes/teaching activities cannot be moved online and must be taught on campus. You should already know if you are in one of these classes/teaching activities and your unit convenor will provide you with more information via iLearn. If you want to confirm, see the list of units with mandatory on-campus classes/teaching activities.

Visit the  $\underline{MQ}$  COVID-19 information page for more detail.

#### **General Information**

Unit convenor and teaching staff

Unit Convener and Lecturer in Charge

Ediz Cetin

#### ediz.cetin@mq.edu.au

Contact via Contact via Email 44 Waterloo Road, Room: 117

Monday's 14:00 – 16:00 hrs.

Credit points

10

Prerequisites

Admission to MEngElecEng and 30cp at 3000 level or above

Corequisites

Co-badged status

Unit description

This unit looks at VLSI technology from the perspective of an enabling platform for digital, analog, and complete system solutions. By taking a systems approach driven by the applications and algorithms, the VLSI design is driven toward a more optimised solution by analysis at higher levels.

### Important Academic Dates

Information about important academic dates including deadlines for withdrawing from units are available at <a href="https://www.mq.edu.au/study/calendar-of-dates">https://www.mq.edu.au/study/calendar-of-dates</a>

## **Learning Outcomes**

On successful completion of this unit, you will be able to:

**ULO1:** Demonstrate an understanding of foundational impact of implementation technology and develop advanced VLSI engineering skills.

**ULO2:** Evaluate systems and algorithms in regard to their implementation as VLSI integrated circuits.

**ULO3:** Incorporate design trade-offs involving area, power and performance as a result of algorithm and architecture selection for practical problems requiring VLSI solutions.

**ULO4:** Demonstrate competency in the practical use of standard VLSI work products for

communication and documentation of engineering processes.

**ULO5:** Execute a project to implement an application or VLSI algorithm and produce requirements, specifications, and designs for low-power, area and/or high performance.

#### **General Assessment Information**

#### Grading and passing requirement for unit

In order to pass this unit a student must obtain a mark of 50 or more for the unit (i.e. obtain a passing grade P/CR/D/HD).

For further details about grading, please refer below in the policies and procedures section.

#### **Hurdle Requirements**

There are no hurdle requirements.

#### Late submissions and Resubmissions

Late submissions will attract a penalty of 10% marks per day. Extenuating circumstances will be considered upon lodgment of a formal notice of disruption of studies.

Once an assignment submission has closed no resubmission of assignments will be permitted.

#### **Assessment Tasks**

Name	Weighting	Hurdle	Due
Assignment 1	10%	No	Week 4
Assignment 1 Defence	15%	No	Week 4
Assignment 2	10%	No	Week 7
Assignment 2 Defence	15%	No	Week 7
Project Report	20%	No	Week 13
Project Defence	30%	No	Exam Period

### **Assignment 1**

Assessment Type 1: Report

Indicative Time on Task 2: 15 hours

Due: Week 4 Weighting: 10%

Assignment 1 Report (1000-word equivalent)

On successful completion you will be able to:

- Demonstrate an understanding of foundational impact of implementation technology and develop advanced VLSI engineering skills.
- Evaluate systems and algorithms in regard to their implementation as VLSI integrated circuits.
- Incorporate design trade-offs involving area, power and performance as a result of algorithm and architecture selection for practical problems requiring VLSI solutions.
- Demonstrate competency in the practical use of standard VLSI work products for communication and documentation of engineering processes.

### Assignment 1 Defence

Assessment Type 1: Viva/oral examination

Indicative Time on Task 2: 5 hours

Due: **Week 4** Weighting: **15%** 

Assignment 1 Defence

On successful completion you will be able to:

- Demonstrate an understanding of foundational impact of implementation technology and develop advanced VLSI engineering skills.
- Evaluate systems and algorithms in regard to their implementation as VLSI integrated circuits.
- Incorporate design trade-offs involving area, power and performance as a result of algorithm and architecture selection for practical problems requiring VLSI solutions.
- Demonstrate competency in the practical use of standard VLSI work products for communication and documentation of engineering processes.

### Assignment 2

Assessment Type 1: Report

Indicative Time on Task 2: 15 hours

Due: Week 7
Weighting: 10%

Assignment 2 Report (1000-word equivalent)

On successful completion you will be able to:

- Demonstrate an understanding of foundational impact of implementation technology and develop advanced VLSI engineering skills.
- Evaluate systems and algorithms in regard to their implementation as VLSI integrated circuits.
- Incorporate design trade-offs involving area, power and performance as a result of algorithm and architecture selection for practical problems requiring VLSI solutions.
- Demonstrate competency in the practical use of standard VLSI work products for communication and documentation of engineering processes.

## Assignment 2 Defence

Assessment Type 1: Viva/oral examination Indicative Time on Task 2: 5 hours

Due: Week 7 Weighting: 15%

Assignment 2 Defence

On successful completion you will be able to:

- Demonstrate an understanding of foundational impact of implementation technology and develop advanced VLSI engineering skills.
- Evaluate systems and algorithms in regard to their implementation as VLSI integrated circuits.
- Incorporate design trade-offs involving area, power and performance as a result of algorithm and architecture selection for practical problems requiring VLSI solutions.
- Demonstrate competency in the practical use of standard VLSI work products for communication and documentation of engineering processes.

#### **Project Report**

Assessment Type 1: Report

Indicative Time on Task 2: 35 hours

Due: Week 13 Weighting: 20%

Project Report (2000-word equivalent)

On successful completion you will be able to:

- Demonstrate an understanding of foundational impact of implementation technology and develop advanced VLSI engineering skills.
- Evaluate systems and algorithms in regard to their implementation as VLSI integrated circuits.
- Incorporate design trade-offs involving area, power and performance as a result of algorithm and architecture selection for practical problems requiring VLSI solutions.
- Demonstrate competency in the practical use of standard VLSI work products for communication and documentation of engineering processes.
- Execute a project to implement an application or VLSI algorithm and produce requirements, specifications, and designs for low-power, area and/or high performance.

## **Project Defence**

Assessment Type 1: Viva/oral examination Indicative Time on Task 2: 10 hours

Due: **Exam Period** Weighting: **30%** 

**Project Defence** 

On successful completion you will be able to:

- Demonstrate an understanding of foundational impact of implementation technology and develop advanced VLSI engineering skills.
- Evaluate systems and algorithms in regard to their implementation as VLSI integrated circuits.
- Incorporate design trade-offs involving area, power and performance as a result of algorithm and architecture selection for practical problems requiring VLSI solutions.
- Demonstrate competency in the practical use of standard VLSI work products for communication and documentation of engineering processes.
- Execute a project to implement an application or VLSI algorithm and produce requirements, specifications, and designs for low-power, area and/or high performance.

- the academic teaching staff in your unit for guidance in understanding or completing this type of assessment
- · the Writing Centre for academic skills support.

## **Delivery and Resources**

Textbook: None required to purchase. Lecturer will provide the reading material.

Suggested references: Keshab K. Parhi, "VLSI Digital Signal Processing Systems: Design and Implementation", Wiley, 1998.

#### **Policies and Procedures**

Macquarie University policies and procedures are accessible from Policy Central (https://policies.mq.edu.au). Students should be aware of the following policies in particular with regard to Learning and Teaching:

- Academic Appeals Policy
- Academic Integrity Policy
- Academic Progression Policy
- Assessment Policy
- · Fitness to Practice Procedure
- Grade Appeal Policy
- Complaint Management Procedure for Students and Members of the Public
- Special Consideration Policy

Students seeking more policy resources can visit <u>Student Policies</u> (<u>https://students.mq.edu.au/support/study/policies</u>). It is your one-stop-shop for the key policies you need to know about throughout your undergraduate student journey.

To find other policies relating to Teaching and Learning, visit Policy Central (https://policies.mq.e du.au) and use the search tool.

#### **Student Code of Conduct**

Macquarie University students have a responsibility to be familiar with the Student Code of Conduct: <a href="https://students.mq.edu.au/admin/other-resources/student-conduct">https://students.mq.edu.au/admin/other-resources/student-conduct</a>

#### Results

Results published on platform other than <u>eStudent</u>, (eg. iLearn, Coursera etc.) or released directly by your Unit Convenor, are not confirmed as they are subject to final approval by the University. Once approved, final results will be sent to your student email address and will be made available in <u>eStudent</u>. For more information visit <u>ask.mg.edu.au</u> or if you are a Global MBA

<sup>&</sup>lt;sup>1</sup> If you need help with your assignment, please contact:

<sup>&</sup>lt;sup>2</sup> Indicative time-on-task is an estimate of the time required for completion of the assessment task and is subject to individual variation

student contact globalmba.support@mq.edu.au

### Student Support

Macquarie University provides a range of support services for students. For details, visit <a href="http://students.mq.edu.au/support/">http://students.mq.edu.au/support/</a>

#### **Learning Skills**

Learning Skills (mq.edu.au/learningskills) provides academic writing resources and study strategies to help you improve your marks and take control of your study.

- · Getting help with your assignment
- Workshops
- StudyWise
- Academic Integrity Module

The Library provides online and face to face support to help you find and use relevant information resources.

- · Subject and Research Guides
- · Ask a Librarian

### Student Services and Support

Students with a disability are encouraged to contact the <u>Disability Service</u> who can provide appropriate help with any issues that arise during their studies.

### Student Enquiries

For all student enquiries, visit Student Connect at ask.mq.edu.au

If you are a Global MBA student contact globalmba.support@mq.edu.au

## IT Help

For help with University computer systems and technology, visit <a href="http://www.mq.edu.au/about\_us/">http://www.mq.edu.au/about\_us/</a> offices and units/information technology/help/.

When using the University's IT, you must adhere to the <u>Acceptable Use of IT Resources Policy</u>. The policy applies to all who connect to the MQ network including students.

# **Changes from Previous Offering**

Minor updates to the content.