



# ELEC4092

## Electrical and Electronic Engineering Research Thesis A

Session 2, In person-scheduled-weekday, North Ryde 2022

*School of Engineering*

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#### Disclaimer

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## General Information

Unit convenor and teaching staff

Unit Convenor

Hazer Inaltekin

[hazer.inaltekin@mq.edu.au](mailto:hazer.inaltekin@mq.edu.au)

Contact via 9850 2280

44 WTR, Room 133

Wednesday 5pm-6pm

Credit points

10

Prerequisites

(ELEC324 or ELEC3024) and ((ELEC342 or ELEC3042) or ((ELCT3005 or ELEC395) and (ELCT3006 or ELEC396)))

Corequisites

Co-badged status

Unit description

In this unit students will conduct the first half of an individual research thesis on a topic in the Electrical and Electronic Engineering specialisation under the direction of an academic supervisor. The focus of the work will be on developing the project proposal, conducting the literature review and project planning and design.

## Important Academic Dates

Information about important academic dates including deadlines for withdrawing from units are available at <https://www.mq.edu.au/study/calendar-of-dates>

## Learning Outcomes

On successful completion of this unit, you will be able to:

**ULO1:** Analyse a complex Electronics Engineering problem, and propose solutions involving the development of new knowledge or the application of cutting edge techniques.

**ULO2:** Plan a major Electronics Engineering research project, including the design of necessary processes, information management, records keeping, project management, and communications.

**ULO3:** Demonstrate an advanced knowledge of contextual factors, research direction, and foundational concepts in Electronics Engineering.

**ULO4:** Apply core Electronics Engineering principles and practices to a research or industry challenge.

**ULO5:** Demonstrate intellectual independence and an in-depth understanding of a specialist topic within Electronics Engineering, through verbal and written communication.

## General Assessment Information

### Grading and passing requirement for unit

In order to pass this unit a student must obtain a mark of 50 or more for the unit (i.e. obtain a passing grade P/ CR/ D/ HD).

For further details about grading, please refer below in the policies and procedures section.

### Hurdle Requirements

The Preliminary Thesis is a hurdle requirement. A grade of 50% or more on the Preliminary Thesis is a condition of passing this unit. If you are given a second opportunity to submit your thesis as a result of failing to meet the minimum mark required, your submission will be due during the supplementary examination period and will be notified of the exact day and time by the unit convenor. The second attempt at a hurdle assessment is graded as pass fail. The maximum grade for a second attempt is the hurdle threshold grade.

Regular meetings with thesis supervisor is a hurdle requirement. Students are required to attend at least 5 out of 10 weekly meetings from Week 4 to Week 13. See details in assessment task description.

### Late submissions and Re-submissions

All assessments must be submitted by 23:59pm (Sydney Time) on their due date. Should the activities be missed due to illness or misadventure, students may apply for Special Consideration, as detailed below.

- **Preliminary Thesis Report:** Late penalty applies unless there is an approved special consideration request. Resubmissions are not allowed.
- **Presentation:** Late submissions are not allowed unless there is an approved special consideration request. Special considerations for presentations are approved only if there are long delays due to extenuating circumstances. Resubmissions are not allowed.
- **Logbooks (Management and Engagement):** Late penalty applies unless there is an approved special consideration request. Resubmissions are not allowed.

### Project Implementation

If your project requires on-campus lab attendance and you are not able to get back to campus on time, please contact with the unit convenor and your supervisor as soon as possible.

## Assessment Tasks

Name	Weighting	Hurdle	Due
<u>Meeting with Supervisors</u>	0%	Yes	Week 13
<u>Presentation</u>	20%	No	Week 14-16
<u>Management and Engagement</u>	10%	No	All Session
<u>Preliminary Thesis</u>	70%	Yes	Week 13

### Meeting with Supervisors

Assessment Type <sup>1</sup>: Participatory task

Indicative Time on Task <sup>2</sup>: 5 hours

Due: **Week 13**

Weighting: **0%**

**This is a hurdle assessment task (see assessment policy for more information on hurdle assessment tasks)**

Students are required to meet with their supervisors on a weekly basis, once the project commences. Such weekly meetings should aim to seek feedback and steer the project, and would normally last at least 15-30 minutes or more. In order to pass this unit, a student must attend at least 5 out of 10 weekly meetings from Week 4 to Week 13. Meetings can be conducted using telephone or video-conference. Meetings should be logged using the consultation meeting log sheet provided on iLearn.

On successful completion you will be able to:

- Analyse a complex Electronics Engineering problem, and propose solutions involving the development of new knowledge or the application of cutting edge techniques.
- Plan a major Electronics Engineering research project, including the design of necessary processes, information management, records keeping, project management, and communications.
- Demonstrate an advanced knowledge of contextual factors, research direction, and foundational concepts in Electronics Engineering.
- Apply core Electronics Engineering principles and practices to a research or industry challenge.

- Demonstrate intellectual independence and an in-depth understanding of a specialist topic within Electronics Engineering, through verbal and written communication.

## Presentation

Assessment Type <sup>1</sup>: Presentation

Indicative Time on Task <sup>2</sup>: 10 hours

Due: **Week 14-16**

Weighting: **20%**

Students are required to deliver a comprehensive oral presentation about their project progress at the end of the unit.

On successful completion you will be able to:

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- Plan a major Electronics Engineering research project, including the design of necessary processes, information management, records keeping, project management, and communications.
- Demonstrate an advanced knowledge of contextual factors, research direction, and foundational concepts in Electronics Engineering.
- Apply core Electronics Engineering principles and practices to a research or industry challenge.
- Demonstrate intellectual independence and an in-depth understanding of a specialist topic within Electronics Engineering, through verbal and written communication.

## Management and Engagement

Assessment Type <sup>1</sup>: Participatory task

Indicative Time on Task <sup>2</sup>: 10 hours

Due: **All Session**

Weighting: **10%**

Students are required to actively engage with the project-related activities, and to demonstrate a professional demeanour towards project management and record-keeping. Students are also required to maintain a logbook for this unit, where dated records of day-to-day activities associated with the project are maintained.

On successful completion you will be able to:

- Analyse a complex Electronics Engineering problem, and propose solutions involving the development of new knowledge or the application of cutting edge techniques.
- Plan a major Electronics Engineering research project, including the design of necessary processes, information management, records keeping, project management, and communications.
- Demonstrate an advanced knowledge of contextual factors, research direction, and foundational concepts in Electronics Engineering.
- Apply core Electronics Engineering principles and practices to a research or industry challenge.
- Demonstrate intellectual independence and an in-depth understanding of a specialist topic within Electronics Engineering, through verbal and written communication.

## Preliminary Thesis

Assessment Type <sup>1</sup>: Thesis

Indicative Time on Task <sup>2</sup>: 50 hours

Due: **Week 13**

Weighting: **70%**

**This is a hurdle assessment task (see [assessment policy](#) for more information on hurdle assessment tasks)**

Students are required to prepare a preliminary thesis report about their projects, including the literature review, project planning & design, progress and achievements.

On successful completion you will be able to:

- Analyse a complex Electronics Engineering problem, and propose solutions involving the development of new knowledge or the application of cutting edge techniques.
- Plan a major Electronics Engineering research project, including the design of necessary processes, information management, records keeping, project management, and communications.
- Demonstrate an advanced knowledge of contextual factors, research direction, and foundational concepts in Electronics Engineering.
- Apply core Electronics Engineering principles and practices to a research or industry challenge.
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topic within Electronics Engineering, through verbal and written communication.

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<sup>1</sup> If you need help with your assignment, please contact:

- the academic teaching staff in your unit for guidance in understanding or completing this type of assessment
- the [Writing Centre](#) for academic skills support.

<sup>2</sup> Indicative time-on-task is an estimate of the time required for completion of the assessment task and is subject to individual variation

## Delivery and Resources

### Unit Delivery

This is a project-based unit and has no scheduled lectures or tutorial sessions. Special lectures may be organised and related announcements will be made via iLearn.

### Logbook

This unit requires a logbook. The students should maintain an individual logbook which should contain a dated log of day-to-day activities undertaken in relation to the project.

### Technology Used and Required

The students are required to discuss with their supervisor about the software/hardware resources required for analysis, simulation, testing and experiments related to their project. In addition, word processing software (MS Word, Latex etc.) will be required to produce the preliminary thesis and MS PowerPoint or equivalent software will be required for presentation slides.

### Unit Webpage

Access from the online iLearn System at <http://ilearn.mq.edu.au>

### Required and Recommended Texts/Materials

There is not set textbook for this unit. The students are required to discuss with their supervisor regarding required/recommended reading materials, as suited to individual project needs.

## Unit Schedule

Refer to iLearn for more details.

## Policies and Procedures

Macquarie University policies and procedures are accessible from [Policy Central](https://policies.mq.edu.au) (<https://policies.mq.edu.au>). Students should be aware of the following policies in particular with regard to Learning and Teaching:

- [Academic Appeals Policy](#)

- [Academic Integrity Policy](#)
- [Academic Progression Policy](#)
- [Assessment Policy](#)
- [Fitness to Practice Procedure](#)
- [Assessment Procedure](#)
- [Complaints Resolution Procedure for Students and Members of the Public](#)
- [Special Consideration Policy](#)

Students seeking more policy resources can visit [Student Policies \(https://students.mq.edu.au/support/study/policies\)](https://students.mq.edu.au/support/study/policies). It is your one-stop-shop for the key policies you need to know about throughout your undergraduate student journey.

To find other policies relating to Teaching and Learning, visit [Policy Central \(https://policies.mq.edu.au\)](https://policies.mq.edu.au) and use the [search tool](#).

## Student Code of Conduct

Macquarie University students have a responsibility to be familiar with the Student Code of Conduct: <https://students.mq.edu.au/admin/other-resources/student-conduct>

## Results

Results published on platform other than [eStudent](#), (eg. iLearn, Coursera etc.) or released directly by your Unit Convenor, are not confirmed as they are subject to final approval by the University. Once approved, final results will be sent to your student email address and will be made available in [eStudent](#). For more information visit [ask.mq.edu.au](https://ask.mq.edu.au) or if you are a Global MBA student contact [globalmba.support@mq.edu.au](mailto:globalmba.support@mq.edu.au)

## Academic Integrity

At Macquarie, we believe [academic integrity](#) – honesty, respect, trust, responsibility, fairness and courage – is at the core of learning, teaching and research. We recognise that meeting the expectations required to complete your assessments can be challenging. So, we offer you a range of resources and services to help you reach your potential, including free [online writing and maths support](#), [academic skills development](#) and [wellbeing consultations](#).

## Student Support

Macquarie University provides a range of support services for students. For details, visit <http://students.mq.edu.au/support/>

## The Writing Centre

[The Writing Centre](#) provides resources to develop your English language proficiency, academic writing, and communication skills.

- [Workshops](#)
- [Chat with a WriteWISE peer writing leader](#)
- [Access StudyWISE](#)



- [Upload an assignment to Studiosity](#)
- [Complete the Academic Integrity Module](#)

The Library provides online and face to face support to help you find and use relevant information resources.

- [Subject and Research Guides](#)
- [Ask a Librarian](#)

## Student Services and Support

Macquarie University offers a range of [Student Support Services](#) including:

- [IT Support](#)
- [Accessibility and disability support](#) with study
- Mental health [support](#)
- [Safety support](#) to respond to bullying, harassment, sexual harassment and sexual assault
- [Social support including information about finances, tenancy and legal issues](#)

## Student Enquiries

Got a question? Ask us via [AskMQ](#), or contact [Service Connect](#).

## IT Help

For help with University computer systems and technology, visit [http://www.mq.edu.au/about\\_us/offices\\_and\\_units/information\\_technology/help/](http://www.mq.edu.au/about_us/offices_and_units/information_technology/help/).

When using the University's IT, you must adhere to the [Acceptable Use of IT Resources Policy](#). The policy applies to all who connect to the MQ network including students.

## Changes from Previous Offering

N/A

## Engineers Australia Competency Mapping

EA Competency Standard		Unit Learning Outcomes
Knowledge and Skill Base	1.1 Comprehensive, theory-based understanding of the underpinning fundamentals applicable to the engineering discipline.	
	1.2 Conceptual understanding of underpinning maths, analysis, statistics, computing.	

	1.3 In-depth understanding of specialist bodies of knowledge	ULO1, ULO3
	1.4 Discernment of knowledge development and research directions	ULO3
	1.5 Knowledge of engineering design practice	ULO3
	1.6 Understanding of scope, principles, norms, accountabilities of sustainable engineering practice.	ULO3
Engineering Application Ability	2.1 Application of established engineering methods to complex problem solving	ULO1, ULO4
	2.2 Fluent application of engineering techniques, tools and resources.	ULO4
	2.3 Application of systematic engineering synthesis and design processes.	ULO4
	2.4 Application of systematic approaches to the conduct and management of engineering projects.	ULO2, ULO4
Professional and Personal Attributes	3.1 Ethical conduct and professional accountability.	
	3.2 Effective oral and written communication in professional and lay domains.	ULO2, ULO5
	3.3 Creative, innovative and pro-active demeanour.	ULO5
	3.4 Professional use and management of information.	ULO2
	3.5 Orderly management of self, and professional conduct.	ULO2, ULO5
	3.6 Effective team membership and team leadership	