



ELEC410

Electronics Engineering Research Thesis A

S2 Day 2019

School of Engineering

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General Information

Unit convenor and teaching staff

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Credit points

6

Prerequisites

69cp at 100 level or above including ELEC342 and ELEC343 and ELEC376

Corequisites

ELEC446 or ELEC476

Co-badged status

Unit description

In this unit students will conduct the first half of an individual research thesis on a topic in the Electronics Engineering major under the direction of an academic supervisor. The focus of the work will be on the developing the project proposal, conducting the literature review and project planning and design.

Important Academic Dates

Information about important academic dates including deadlines for withdrawing from units are available at <https://www.mq.edu.au/study/calendar-of-dates>

Learning Outcomes

On successful completion of this unit, you will be able to:

Demonstrate intellectual independence and an in-depth understanding of a specialist

topic within Electronics Engineering, through verbal and written communication.

Analyse a complex Electronics Engineering problem, and propose solutions involving the development of new knowledge or the application of cutting edge techniques.

Plan a major Electronics Engineering research project, including the design of necessary processes, information management, records keeping, project management, and communications.

Demonstrate an advanced knowledge of contextual factors, research direction, and foundational concepts in Electronics Engineering.

Apply core Electronics Engineering principles and practices to a research or industry challenge.

General Assessment Information

Grading and passing requirement for unit

In order to pass this unit a student must obtain a mark of 50 or more for the unit (i.e. obtain a passing grade P/ CR/ D/ HD).

For further details about grading, please refer below in the policies and procedures section.

Hurdle Requirements

The Preliminary Thesis is a hurdle requirement. A grade of 50% or more on the Preliminary Thesis is a condition of passing this unit. If you are given a second opportunity to submit your thesis as a result of failing to meet the minimum mark required, your submission will be due during the supplementary examination period and will be notified of the exact day and time by the unit convenor. The second attempt at a hurdle assessment is graded as pass fail. The maximum grade for a second attempt is the hurdle threshold grade.

Regular meetings with thesis supervisor is a hurdle requirement. See details in assessment task description.

Late submissions and Re-submissions

Late submissions will attract a penalty of 10% marks per day. Extenuating circumstances will be considered upon lodgement of a special consideration application.

Resubmissions of work are not allowed after due date.

Assessment Tasks

Name	Weighting	Hurdle	Due
<u>Preliminary Thesis</u>	70%	Yes	Week 13
<u>Management and Engagement</u>	10%	No	All Session

Name	Weighting	Hurdle	Due
<u>Meetings with Supervisors</u>	0%	Yes	Week 13
<u>Presentation</u>	20%	No	Week 14-16

Preliminary Thesis

Due: **Week 13**

Weighting: **70%**

This is a hurdle assessment task (see assessment policy for more information on hurdle assessment tasks)

Refer to iLearn for guidelines.

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Management and Engagement

Due: **All Session**

Weighting: **10%**

Students are required to actively engage with the project-related activities, and to demonstrate a professional demeanour towards project management and record-keeping. Students are also required to maintain a logbook for this unit, where dated records of day-to-day activities associated with the project are maintained. More guidelines are provided on iLearn.

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development of new knowledge or the application of cutting edge techniques.

- Plan a major Electronics Engineering research project, including the design of necessary processes, information management, records keeping, project management, and communications.
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- Apply core Electronics Engineering principles and practices to a research or industry challenge.

Meetings with Supervisors

Due: **Week 13**

Weighting: **0%**

This is a hurdle assessment task (see [assessment policy](#) for more information on hurdle assessment tasks)

Students are expected to meet with their supervisor on a weekly basis, once the project commences. Such weekly meetings should aim to seek feedback and steer the project, and would normally last at least 15-30 minutes or more. In order to pass this unit, a student must attend at least 5 out of 10 weekly meetings between Week 4 to Week 12. In case a face-to-face meeting is not possible, a meeting must be conducted using telephone or video-conference. Meetings should be logged using the consultation meeting log sheet provided on iLearn.

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- Plan a major Electronics Engineering research project, including the design of necessary processes, information management, records keeping, project management, and communications.
- Demonstrate an advanced knowledge of contextual factors, research direction, and foundational concepts in Electronics Engineering.
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Presentation

Due: **Week 14-16**

Weighting: **20%**

Refer to iLearn for guidelines and schedule.

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Delivery and Resources

Unit Delivery

This is a project-based unit and has no scheduled lectures or tutorial sessions. Special lectures may be organised and related announcements will be made via iLearn.

Logbook

This unit requires a logbook. The students should maintain an individual logbook which should contain a dated log of day-to-day activities undertaken in relation to the project.

Technology Used and Required

The students are required to discuss with their supervisor about the software/hardware resources required for analysis, simulation, testing and experiments related to their project. In addition, word processing software (MS Word, Latex etc.) will be required to produce the preliminary thesis and MS PowerPoint or equivalent software will be required for presentation slides.

Unit Webpage: Access from the online iLearn System at <http://ilearn.mq.edu.au>

Required and Recommended Texts/Materials

There is not set textbook for this unit. The students are required to discuss with their supervisor regarding required/recommended reading materials, as suited to individual project needs.

Policies and Procedures

Macquarie University policies and procedures are accessible from [Policy Central](https://staff.mq.edu.au/work/strategy-planning-and-governance/university-policies-and-procedures/policy-central) (<https://staff.mq.edu.au/work/strategy-planning-and-governance/university-policies-and-procedures/policy-central>). Students should be aware of the following policies in particular with regard to Learning and Teaching:

- [Academic Appeals Policy](#)
- [Academic Integrity Policy](#)
- [Academic Progression Policy](#)
- [Assessment Policy](#)
- [Fitness to Practice Procedure](#)
- [Grade Appeal Policy](#)
- [Complaint Management Procedure for Students and Members of the Public](#)
- [Special Consideration Policy](#) (**Note:** *The Special Consideration Policy is effective from 4 December 2017 and replaces the Disruption to Studies Policy.*)

Undergraduate students seeking more policy resources can visit the [Student Policy Gateway](https://students.mq.edu.au/support/study/student-policy-gateway) (<https://students.mq.edu.au/support/study/student-policy-gateway>). It is your one-stop-shop for the key policies you need to know about throughout your undergraduate student journey.

If you would like to see all the policies relevant to Learning and Teaching visit [Policy Central](http://staff.mq.edu.au/work/strategy-planning-and-governance/university-policies-and-procedures/policy-central) (<http://staff.mq.edu.au/work/strategy-planning-and-governance/university-policies-and-procedures/policy-central>).

Student Code of Conduct

Macquarie University students have a responsibility to be familiar with the Student Code of Conduct: <https://students.mq.edu.au/study/getting-started/student-conduct>

Results

Results published on platform other than [eStudent](#), (eg. iLearn, Coursera etc.) or released directly by your Unit Convenor, are not confirmed as they are subject to final approval by the University. Once approved, final results will be sent to your student email address and will be made available in [eStudent](#). For more information visit ask.mq.edu.au or if you are a Global MBA student contact globalmba.support@mq.edu.au

Student Support

Macquarie University provides a range of support services for students. For details, visit <http://students.mq.edu.au/support/>

Learning Skills

Learning Skills (mq.edu.au/learningskills) provides academic writing resources and study strategies to improve your marks and take control of your study.

- [Workshops](#)
- [StudyWise](#)
- [Academic Integrity Module for Students](#)
- [Ask a Learning Adviser](#)

Student Services and Support

Students with a disability are encouraged to contact the [Disability Service](#) who can provide appropriate help with any issues that arise during their studies.

Student Enquiries

For all student enquiries, visit Student Connect at ask.mq.edu.au

If you are a Global MBA student contact globalmba.support@mq.edu.au

IT Help

For help with University computer systems and technology, visit http://www.mq.edu.au/about_us/offices_and_units/information_technology/help/.

When using the University's IT, you must adhere to the [Acceptable Use of IT Resources Policy](#). The policy applies to all who connect to the MQ network including students.

Graduate Capabilities

Creative and Innovative

Our graduates will also be capable of creative thinking and of creating knowledge. They will be imaginative and open to experience and capable of innovation at work and in the community. We want them to be engaged in applying their critical, creative thinking.

This graduate capability is supported by:

Learning outcomes

- Demonstrate intellectual independence and an in-depth understanding of a specialist topic within Electronics Engineering, through verbal and written communication.
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Capable of Professional and Personal Judgement and Initiative

We want our graduates to have emotional intelligence and sound interpersonal skills and to demonstrate discernment and common sense in their professional and personal judgement. They will exercise initiative as needed. They will be capable of risk assessment, and be able to

handle ambiguity and complexity, enabling them to be adaptable in diverse and changing environments.

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- Demonstrate an advanced knowledge of contextual factors, research direction, and foundational concepts in Electronics Engineering.

Commitment to Continuous Learning

Our graduates will have enquiring minds and a literate curiosity which will lead them to pursue knowledge for its own sake. They will continue to pursue learning in their careers and as they participate in the world. They will be capable of reflecting on their experiences and relationships with others and the environment, learning from them, and growing - personally, professionally and socially.

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- Demonstrate an advanced knowledge of contextual factors, research direction, and foundational concepts in Electronics Engineering.

Discipline Specific Knowledge and Skills

Our graduates will take with them the intellectual development, depth and breadth of knowledge, scholarly understanding, and specific subject content in their chosen fields to make them competent and confident in their subject or profession. They will be able to demonstrate, where relevant, professional technical competence and meet professional standards. They will be able

to articulate the structure of knowledge of their discipline, be able to adapt discipline-specific knowledge to novel situations, and be able to contribute from their discipline to inter-disciplinary solutions to problems.

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Critical, Analytical and Integrative Thinking

We want our graduates to be capable of reasoning, questioning and analysing, and to integrate and synthesise learning and knowledge from a range of sources and environments; to be able to critique constraints, assumptions and limitations; to be able to think independently and systemically in relation to scholarly activity, in the workplace, and in the world. We want them to have a level of scientific and information technology literacy.

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- Apply core Electronics Engineering principles and practices to a research or industry challenge.

Problem Solving and Research Capability

Our graduates should be capable of researching; of analysing, and interpreting and assessing data and information in various forms; of drawing connections across fields of knowledge; and they should be able to relate their knowledge to complex situations at work or in the world, in order to diagnose and solve problems. We want them to have the confidence to take the initiative in doing so, within an awareness of their own limitations.

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Effective Communication

We want to develop in our students the ability to communicate and convey their views in forms effective with different audiences. We want our graduates to take with them the capability to read, listen, question, gather and evaluate information resources in a variety of formats, assess, write clearly, speak effectively, and to use visual communication and communication technologies as appropriate.

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- Apply core Electronics Engineering principles and practices to a research or industry challenge.

Engaged and Ethical Local and Global citizens

As local citizens our graduates will be aware of indigenous perspectives and of the nation's historical context. They will be engaged with the challenges of contemporary society and with knowledge and ideas. We want our graduates to have respect for diversity, to be open-minded, sensitive to others and inclusive, and to be open to other cultures and perspectives: they should have a level of cultural literacy. Our graduates should be aware of disadvantage and social justice, and be willing to participate to help create a wiser and better society.

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Socially and Environmentally Active and Responsible

We want our graduates to be aware of and have respect for self and others; to be able to work with others as a leader and a team player; to have a sense of connectedness with others and country; and to have a sense of mutual obligation. Our graduates should be informed and active participants in moving society towards sustainability.

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Learning outcomes

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